



(19)

(11) Publication number:

58060529 A

Generated Document.

PATENT ABSTRACTS OF JAPAN(21) Application number: **56160626**(51) Intl. Cl.: **H01L 21/02**(22) Application date: **06.10.81**

(30) Priority:

(43) Date of application
publication: **11.04.83**(84) Designated contracting
states:(71) Applicant: **MITSUBISHI ELECTRIC CORP**(72) Inventor: **YOSHIDA MIYOSHI
KOTANI KYOHIKO**

(74) Representative:

**(54) IDENTIFYING METHOD
FOR SEMICONDUCTOR
CHIP**

(57) Abstract:

PURPOSE: To improve the efficiency precentage of semiconductor chips, by a method wherein each of chips formed in the same wafer in one lot is provided with an individual mark, thereby allowing each chip to be identified.

CONSTITUTION: Each of a plurality of chips 2 fomed in the same wafer 1 is provided with an individual identification mark 4, e.g., an individual number. After the identification mark 4 is read and stored by means of a TV camera or the like, electrical characteristics of each chip 2 is measured to judge the quality thereof and the rank to which the chip belongs if it is good, and the result is also stored. Upon completion of measurement for all the chips, the wafer 1, together with the mark and judging result of each chip 2, is sent

to the subsequent assembling step,
where the chips cut are sorted
according to their characteristics and
then assembled.

COPYRIGHT: (C)1983,JPO&Japio

